

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): J. NOGUCHI, et al
Application No.: TBD
Filed: December 23, 2003
For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE
Expected Group: 2825
Expected Examiner: B. Keshavan

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 23, 2003

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed in the following, and as set forth on the following pages:

Amendments to the Specification; and

Remarks are included following the amendments.